



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA**  
**KAKINADA – 533 003, Andhra Pradesh, India**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

II Year - I Semester	L	T	P	C
	<b>0</b>	<b>0</b>	<b>3</b>	<b>1.5</b>
<b>ELECTRONIC DEVICES AND CIRCUITS LAB</b>				

**Note:** The students are required to perform the experiment to obtain the V-I characteristics and to determine the relevant parameters from the obtained graphs.

**List of Experiments: (Minimum of Ten Experiments has to be performed)**

1. P-N Junction Diode Characteristics
  - Part A: Germanium Diode (Forward bias & Reverse bias)
  - Part B: Silicon Diode (Forward Bias only)
2. Zener Diode Characteristics
3. Part A: V-I Characteristics
  - Part B: Zener Diode as Voltage Regulator
4. Rectifiers (without and with c-filter)
  - Part A: Half-wave Rectifier
  - Part B: Full-wave Rectifier
5. BJT Characteristics (CE Configuration)
  - Part A: Input Characteristics
  - Part B: Output Characteristics
6. FET Characteristics (CS Configuration)
  - Part A: Drain Characteristics
  - Part B: Transfer Characteristics
7. SCR Characteristics
8. UJT Characteristics
9. Transistor Biasing
10. CRO Operation and its Measurements
11. BJT-CE Amplifier
12. Emitter Follower-CC Amplifier
13. FET-CS Amplifier



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**Equipment required:**

1. Regulated Power supplies
2. Analog/Digital Storage Oscillo scopes
3. Analog/Digital Function Generators
4. Digital Multi-meters
5. Decade Résistance Boxes/Rheostats
6. Decade Capacitance Boxes
7. Ammeters (Analogor Digital)
8. Voltmeters (Analogor Digital)
9. Active & Passive Electronic Components